

30V N-Channel Enhancement Mode Power MOSFET

Description

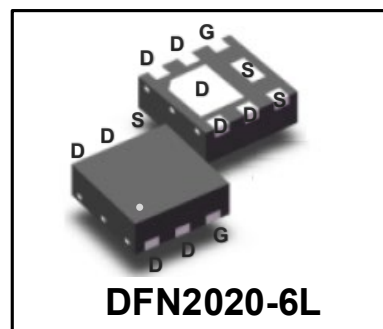
WMR14N03TB uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

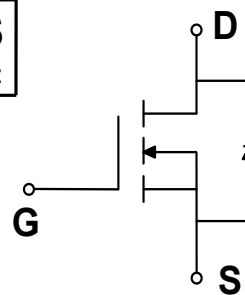
- $V_{DS} = 30V$, $I_D = 13.8A$
 $R_{DS(on)} < 7.5m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(on)} < 10.2m\Omega$ @ $V_{GS} = 4.5V$
- Green Device Available
- High Power and Current Handling Capability

Applications

- Battery Protection
- Power Management
- Load Switch



RoHS
compliant

Absolute Maximum Ratings ($T_A = 25^\circ C$, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_A = 25^\circ C$	I_D	13.8	A
	$T_A = 100^\circ C$		8.7	
Pulsed Drain Current ¹		I_{DM}	55.2	A
Single Pulse Avalanche Energy ²		E_{AS}	24.2	mJ
Total Power Dissipation	$T_A = 25^\circ C$	P_D	2.2	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	$R_{\theta JA}$	56.8	$^\circ C/W$

Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30	-	-	V
Gate-body Leakage current		I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T _J =25°C	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V	-	-	1	μA
	T _J =100°C			-	-	100	
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	1.6	2.5	V
Drain-Source on-Resistance ⁴		R _{DS(on)}	V _{GS} = 10V, I _D = 8A	-	5.7	7.5	mΩ
			V _{GS} = 4.5V, I _D = 6A	-	7.5	10.2	
Dynamic Characteristics ⁵							
Input Capacitance		C _{iss}	V _{DS} = 15V, V _{GS} = 0V, f = 1.0 MHz	-	1195	-	pF
Output Capacitance		C _{oss}		-	188	-	
Reverse Transfer Capacitance		C _{rss}		-	137	-	
Gate Resistance		R _g	f = 1MHz		2.5		Ω
Switching Characteristics ⁵							
Total Gate Charge		Q _g	V _{GS} = 10V, V _{DS} = 15V, I _D = 8A	-	21.5	-	nC
Gate-Source Charge		Q _{gs}		-	3.5	-	
Gate-Drain Charge		Q _{gd}		-	4.5	-	
Turn-on Delay Time		t _{d(on)}	V _{GS} = 10V, V _{DD} = 15V, I _D = 8A, R _G = 3Ω	-	15.6	-	ns
Rise Time		t _r		-	7.2	-	
Turn-off Delay Time		t _{d(off)}		-	31.2	-	
Fall Time		t _f		-	6.8	-	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage ⁴		V _{SD}	I _S = 1A, V _{GS} = 0V	-	-	1.2	V
Continuous Source Current	T _A = 25°C	I _S	-	-	-	13.8	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ\text{C}$.
2. The EAS data shows Max. rating . The test condition is $V_{DD} = 20V, V_{GS} = 10V, L = 0.1\text{mH}, I_{AS} = 22A$.
3. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

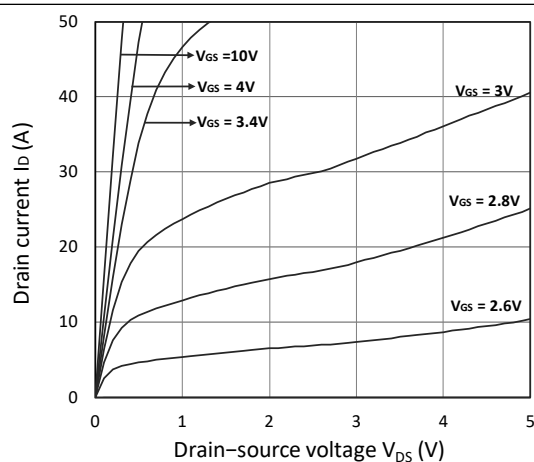


Figure 1. Output Characteristics

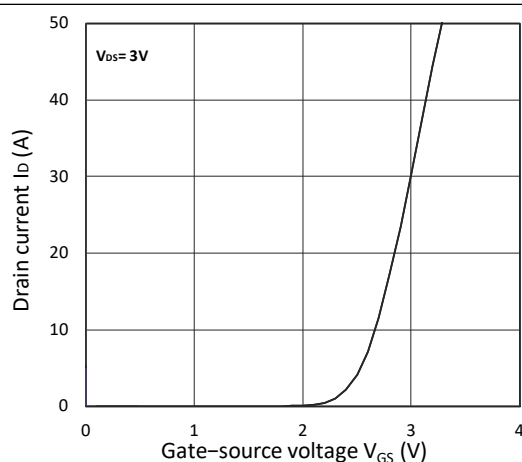


Figure 2. Transfer Characteristics

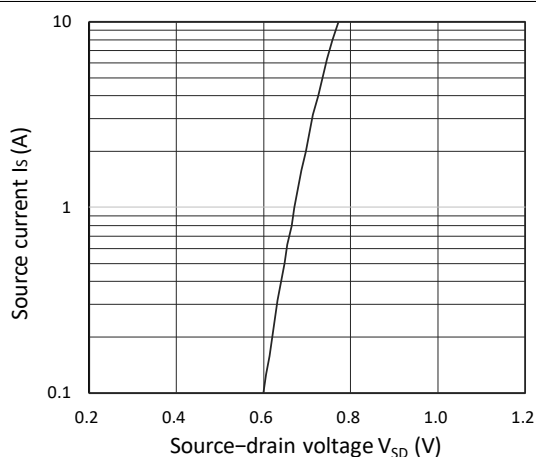
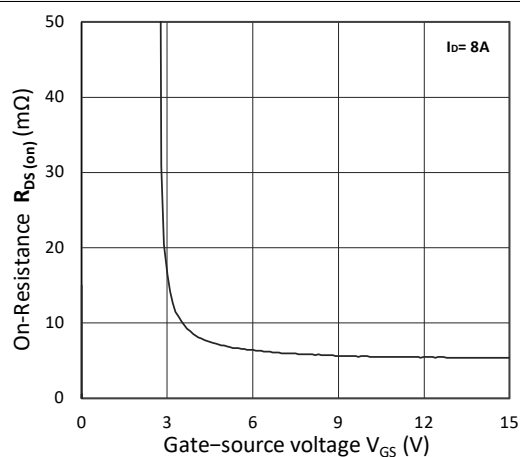
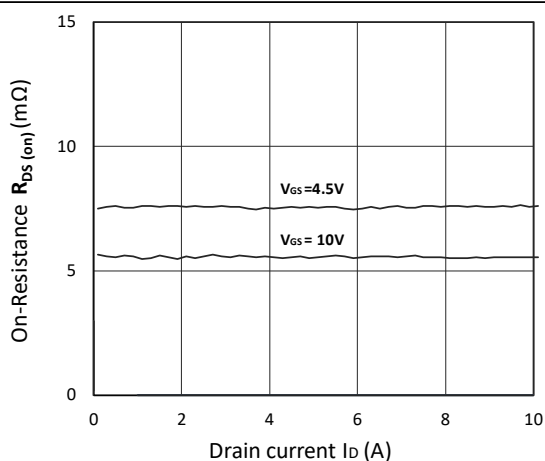
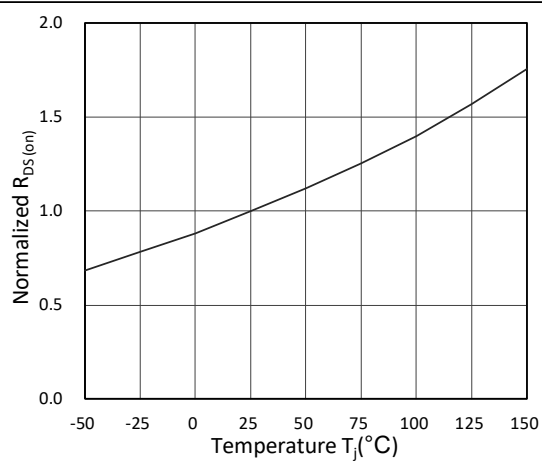


Figure 3. Forward Characteristics of Reverse

Figure 4. $R_{DS(on)}$ vs. V_{GS} Figure 5. $R_{DS(on)}$ vs. I_D Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

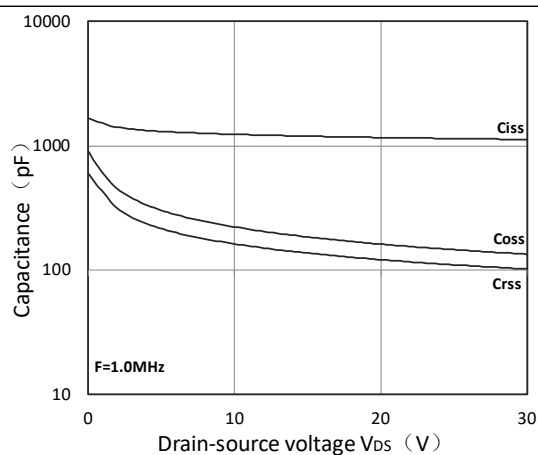


Figure 7. Capacitance Characteristics

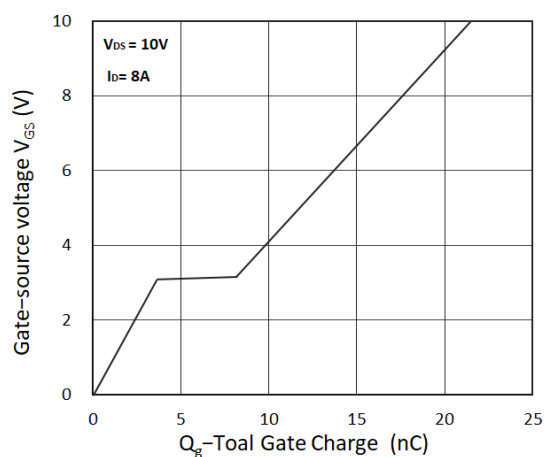


Figure 8. Gate Charge Characteristics

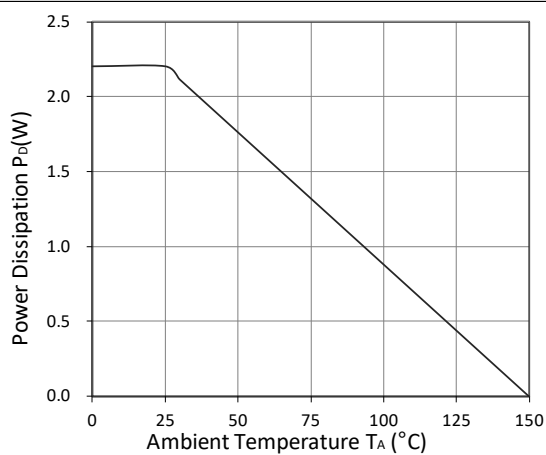


Figure 9. Power Dissipation

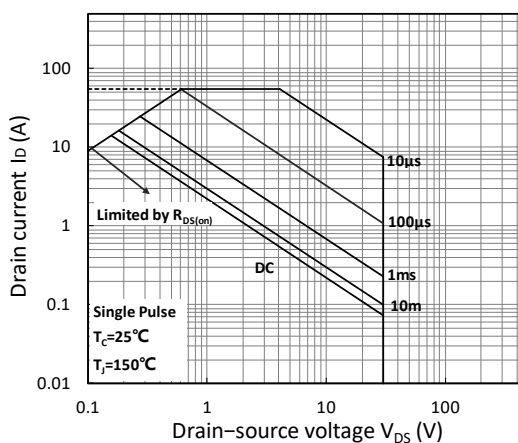


Figure 10. Safe Operating Area

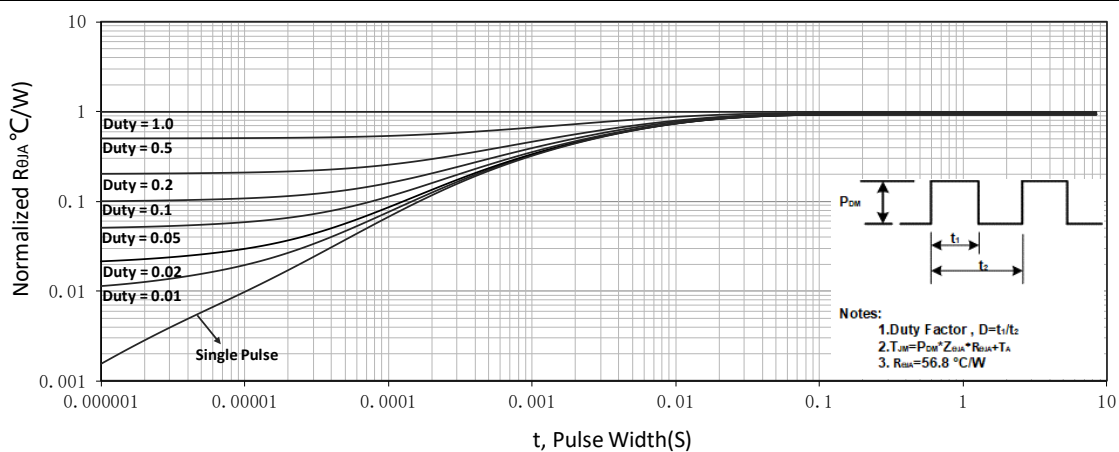


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

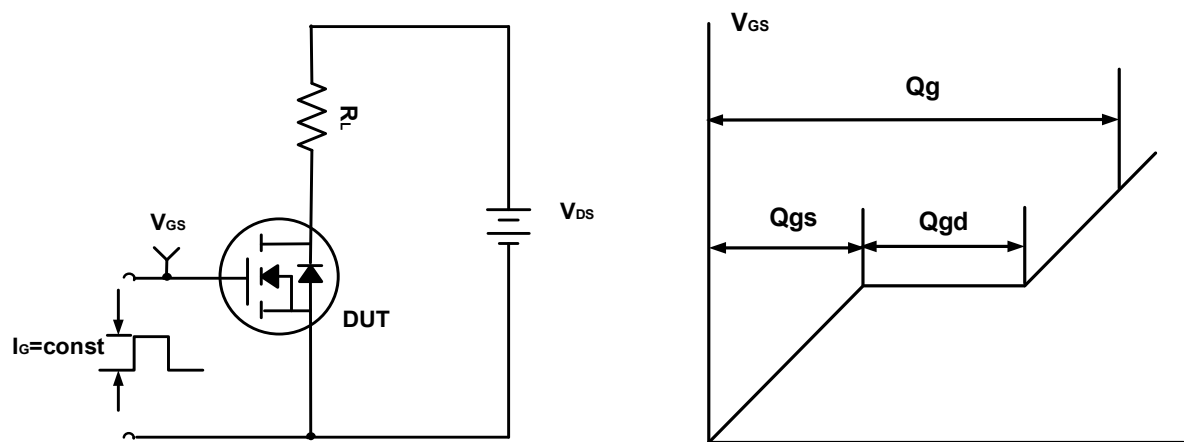


Figure A. Gate Charge Test Circuit & Waveforms

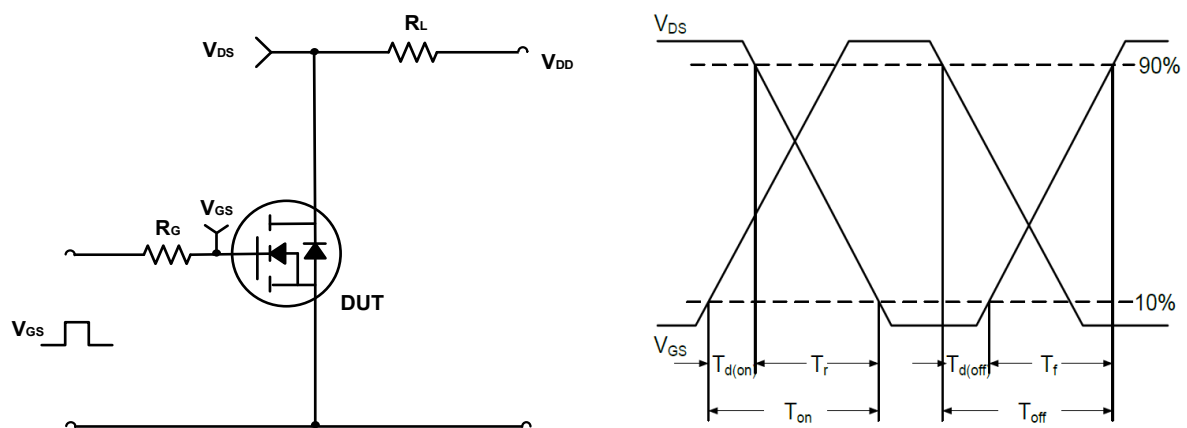


Figure B. Switching Test Circuit & Waveforms

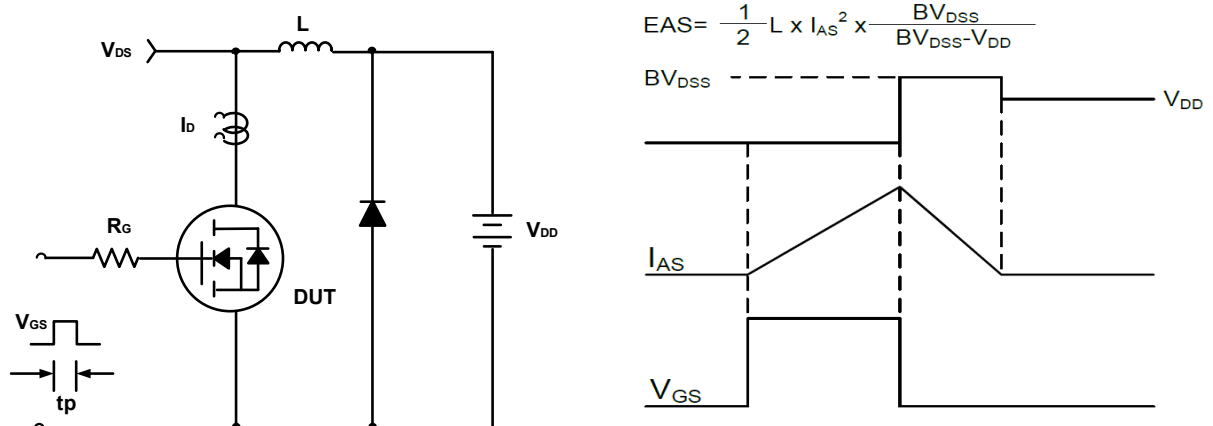
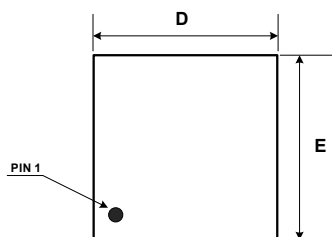
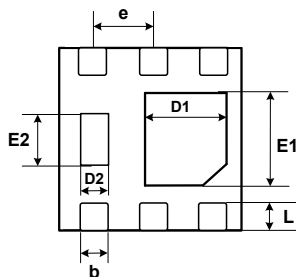


Figure C. Unclamped Inductive Switching Circuit & Waveforms

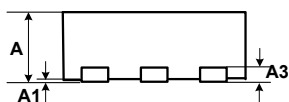
Mechanical Dimensions for DFN2020-6L



Top View



Bottom View



Side View

COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	0.500	0.600
A1	0.000	0.050
A3	0.152REF	
D	1.900	2.100
E	1.900	2.100
D1	0.800	1.000
E1	0.850	1.050
E2	0.460	0.660
D2	0.200	0.400
b	0.250	0.350
e	0.650TYP	
L	0.174	0.326

Ordering Information

Part	Package	Marking	Packing method
WMR14N03TB	DFN2020-6L	R14N03B	Tape and Reel

Marking Information

R14N03B= Device code

WWXXXXX= Date code

Contact Information

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